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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10040460	01-09-2002	257		2811	

**APPLICANTS: Kitahara Terumasa Yasuda Koichi: *TM*

**CONTINUING DATA VERIFIED: *TM*

** FOREIGN APPLICATIONS VERIFIED: *TM*

JAPAN 2001-183911 06/18/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met:		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 024016-00022
Verified and Acknowledged Examiner's initials: <i>TM</i>			
TITLE : Semiconductor integrated circuit device			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs.Drwg.
<input type="checkbox"/> TERMINAL		Print Fig.		
DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE Application Examiner		
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